

IN THE CLAIMS

Please amend the claims as follows:

Claims 1-16 (Canceled).

Claim 17 (Original) An image processing circuit of an image input device which performs a predetermined image processing of image photographed by an image pickup device in said image input device, said circuit comprising:

a real time processing unit in which a predetermined general image processing of a pixel data being photographed by said image pickup device and inputted sequentially is performed by real time processing;

a main memory that stores a pixel data outputted from at least said real time processing unit, in image frame units; and

a central control unit in which with respect to image temporarily stored in said main memory, exceptional image processing except for said general image processing is executed as a software program processing, and then stored in said main memory, characterized in that:

said real time processing unit further comprises:

a gamma compensation table capable of performing, when given a N-bit length pixel data, gamma compensation processing about said pixel data; and

a selector in which, when given a N-bit length pixel data, said pixel data is inputted to said gamma compensation table; and when given a (N-2)-bit length pixel data, switching is made so that four data rows sequentially provided as a (N-2)-bit length pixel data, are respectively inputted to four look-up tables obtained by dividing said gamma compensation table into four.

Claims 18-27 (Canceled).